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APPLICANTS

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\*\* CONTINUING DATA \*\*\*\*\*  
*none*

\*\* FOREIGN APPLICATIONS \*\*\*\*\*  
 JAPAN 2003-044155 02/21/2003  
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Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance <i>Allowance</i>	STATE OR COUNTRY JAPAN	SHEETS DRAWING 17	TOTAL CLAIMS 14	INDEPENDENT CLAIMS 2
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Verified and Acknowledged  
 Examiner's Signature *[Signature]* Initials *sm*

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 22850  
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TITLE  
 Semiconductor device having memory and logic devices with reduced resistance and leakage current

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